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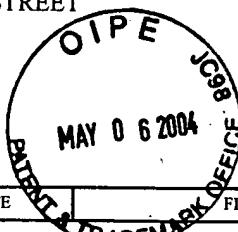
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020457 7590 02/06/2004

ANTONELLI, TERRY, STOUT & KRAUS, LLP
1300 NORTH SEVENTEENTH STREET
SUITE 1800
ARLINGTON, VA 22209-9889



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(Signature)

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/829,969	04/11/2001	Kazuhiro Ohnishi	5000.40010X00	8254

TITLE OF INVENTION: STACKED GATE ELECTRODE FOR A MOS TRANSISTOR OF A SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	05/06/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
TRAN, MAI HUONG C	2818	257-315000

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1 Antonelli, Terry, Stout & Kraus, LLP
 2 _____
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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Renesas Technology Corp.

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): individual corporation or other private group entity government

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(Authorized Signature) Larry V. Anagnos (Date) May 6, 2004

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